

REMARKS

Applicant respectfully requests reconsideration of this application, in view of the following remarks.

Claims 1-11 stand rejected under 35 USC § 102(e) as being anticipated by U.S. Patent 6,115,823 to Velasco et al. (Velasco).

In the Claims

Claims 1-11 stand rejected under 35 USC § 102(e) as being anticipated by U.S. Patent 6,115,823 to Velasco et al. (Velasco).

Claims 1-11 have been amended to more particularly point out and distinctly claim the subject matter that Applicants regard as the invention by redefining a prior limitation in the originally filed claims with additional language to delineate that prior limitation. Support for “at compilation” may be found in the description on page 10, in Figure 6 at step 625, and throughout the specification.

Applicants hereby reserve the right to swear behind the cited references.

Claims 1-11 rejection under 35 U.S.C. § 102(e)

Applicants submit that the Action is equating two different language terms and thereby improperly arriving at an equivalence of elements in the Applicants’ claims and the cited art. As noted, Applicants have amended the claims to more clearly delineate this distinction.

Specifically, the Action on page 4 states:

Regarding claim 1, Applicants argue that “Velasco does not disclose an interface signal or a group of signals that are selectively present at the interface.” However, Velasco clearly teaches at column 35, lines 50-60 that some signals are selectively present. For example, for parametrized RAM size, when a smaller size RAM is used, the signals for the larger size RAM are not used.

(Emphases added.)

The Action has equated the term “selectively present” with “used or not used”. These are different terms and do not equate to one another.

To illustrate this distinction by way of an example, using the parameterized RAM size of Velasco above; when a smaller size RAM is used, the signals for the larger size RAM are not used, however the signals for the larger size RAM are still present. In one embodiment of the Applicants' invention, if a smaller size RAM is used, only the signals for the smaller size RAM are present, the signals for the larger size RAM are not present.

Claim 1 rejection under 35 U.S.C. § 102(e)

Applicants amended claim 1 recites:

1. (Amended) A computer core comprising at least one interface signal carrier that is configurable, at compilation, such that the at least one interface signal carrier can be selectively present.

(Emphasis added.)

Applicants submit that Velasco does not disclose, “at least one interface signal that is configurable, at compilation, such that the interface signal can be selectively present” as in Applicants' claim 1. For this reason Applicants submit that claim 1 is not anticipated by the cited art and that claim 1 and claims dependent on claim 1 are allowable over the cited art.

Claims 4, 6, 7, and 9 rejection under 35 U.S.C. § 102(e)

Claims 4, 6, 7, and 9 claim among other things, the “at compilation” limitation. As discussed above with respect to claim 1, Applicants submit that these claims are not anticipated by the cited art. Applicants submit that claims 4, 6, 7, and 9 and claims dependent on these claims are allowable over the cited art.

CONCLUSION

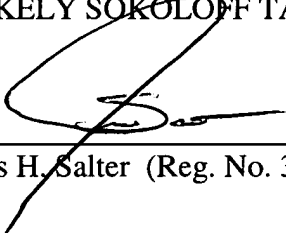
Applicant respectfully submits that all claims are in condition for allowance, and requests allowance of all claims.

The Examiner is invited to call James H. Salter at 408 720-8300 x329 if there remains any issue with allowance.

Please charge any shortage to our Deposit Account No. 02-2666.

Respectfully submitted,  
BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

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